

### Results Key:

**JNL** = Journal or Magazine    **CNF** = Conference    **STD** = Standard

## 1 Characteristics of minimax FIR filters for video interpolation/decimation

*Carrai, P.; Cortelazzo, G.M.; Mian, G.A.;*

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## 2 Video decimator design using a systolic array

*Campbell, S.; Chung, S.M.;*

Circuits and Systems, 1993., ISCAS '93, 1993 IEEE International Symposium on , 3-6 May 1993

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### 3 Video decimator design using a systolic array

Campbell, S.; Chung, S.M.;

Circuits and Systems, 1993., ISCAS '93, 1993 IEEE International Symposium on , 3-6 May 1993

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#### 4 A 2.4 $\mu\text{m}$ CMOS switched-capacitor video decimator with sampling rate reduction from 40.5 MHz to 13.5 MHz

*Martins, R.P.; Franca, J.E.;*

Custom Integrated Circuits Conference, 1989., Proceedings of the IEEE 1989 , 15-18 May 1989

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## 5 Adaptive temporal decimation algorithm with dynamic time window

*Sai-Ho Kwok; Wan-Chi Siu; Constantinides, A.G.;*

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## 6 Two dimensional interpolation of real sequences using the DCT

*Aqbinya, J.I.;*

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**IEEE CNF****10 High performance distributed arithmetic FPGA decimators for video-frequency applications***Living, J.; Al-Hashimi, B.M.; Moniri, M.;*

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**IEEE CNF****11 A new block motion vector estimation using adaptive pixel decimation***Yui-Lam Chan; Wan-Chi Siu;*

Acoustics, Speech, and Signal Processing, 1995. ICASSP-95., 1995 International Conference on , Volume: 4 , 9-12 May 1995

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**IEEE CNF****12 Adaptive multi-feature motion estimation***Crinon, R.J.; Kolodziej, W.J.;*

Acoustics, Speech, and Signal Processing, 1994. ICASSP-94., 1994 IEEE International Conference on , Volume: v , 19-22 April 1994

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**IEEE CNF****13 Joint pre- and post-filter design for decimation-interpolation systems with**

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*Pian, D.T.;*

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## 14 A globally adaptive pixel-decimation algorithm for block-motion estimation

*Yankang Wang; Yanqun Wang; Kuroda, H.;*

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## 15 Motion estimation using low-band-shift method for wavelet-based moving-picture coding

*Hyun-Wook Park; Hyung-Sun Kim;*

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